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UMD2N

Features

- Both the DTA124E chip and DTC124E chip in a package
- Mounting possible with SOT-363 automatic mounting machines.
- Transistor elements are independent, eliminating interference.
- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information)
- Epoxy meets UL 94 V-0 flammability rating
- Moisure Sensitivity Level 1

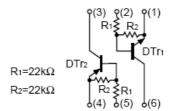
Absolute maximum ratings @ 25 $^{\circ}$

Symbol	Parameter	Value	Unit
V_{cc}	Supply voltage	50	V
V_{IN}	Input voltage	-10~40	V
Ιο	Output current	30	mA
I _{C(MAX)}	Collector Current	100	mA
P_d	Power dissipation	150	mW
Tj	Junction temperature	150	°C
T_{stg}	Storage temperature	-55~150	°C

Electrical Characteristics @ 25 ℃

Symbol	Parameter	Min	Тур	Max	Unit
$V_{I(off)}$	Input voltage (V _{CC} =5V, I _O =100µA)			0.5	V
$V_{I(on)}$	$(V_0=0.2V, I_0=5mA)$	3.0			V
$V_{O(on)}$	Output voltage (I _O /I _I =10mA/0.5mA)		0.1	0.3	V
l _l	Input current (V _I =5V)			0.36	mA
I _{O(off)}	Output current (V _{CC} =50V, V _I =0)			0.5	μA
Gı	DC current gain (V ₀ =5V, I ₀ =5mA)	56			
R₁	Input resistance	15.4	22	28.6	ΚΩ
R ₂ /R ₁	Resistance ratio	0.8	1.0	1.2	
f⊤	Transition frequency (V _{CE} =10V, I _E =5mA, f=100MHz)		250		MHz

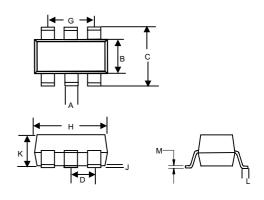
Equivalent circuit



Marking:D2

Digital Transistors

SOT-363



DIMENSIONS						
	INC	HES	MM			
DIM	MIN	MAX	MIN	MAX	NOTE	
Α	.006	.014	0.15	0.35		
В	.045	.053	1.15	1.35		
С	.085	.096	2.15	2.45		
D	.026		0.65Nominal			
G	.047	.055	1.20	1.40		
Η	.071	.087	1.80	2.20		
J		.004		0.10		
K	.035	.043	0.90	1.10		
L	.010	.018	0.26	0.46		
M	.003	.006	0.08	0.15		



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Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel 3Kpcs/Reel

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